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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/660,243	09/11/2003	Matthew S. Shafer	X-1353 US	5366
24309	7590	02/13/2006	EXAMINER CHUNG, PHUNG M	
XILINX, INC ATTN: LEGAL DEPARTMENT 2100 LOGIC DR SAN JOSE, CA 95124			ART UNIT 2138	

DATE MAILED: 02/13/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 10/660,243	<b>Applicant(s)</b> SHAHER ET AL	
	<b>Examiner</b> Phung My Chung	<b>Art Unit</b> 2138	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is FINAL.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-24 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,6,8-10,14-16,22 and 23 is/are rejected.
- 7) ☒ Claim(s) 2-5,7,11-13,17-21 and 24 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |  |
|--|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. ____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>9/11/03</u> . | 6) <input type="checkbox"/> Other: ____  |

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***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 6, 8-10, 14-16 and 22-23 are rejected under 35 U.S.C. 102(b) as being anticipated by Waschura et al (5,414,713).

As per claim 1, Waschura et al disclose a device, comprising:  
configurable logic configured with a pattern generator (40), the pattern generator configured for a test mode;  
configurable input/output (24, 30) interconnect coupled to the configurable logic;  
transceivers (20) coupled to the configurable input/output interconnect; and  
the configurable input/output interconnect configured to communicate test signals from the pattern generator for transmission by at least one transceiver of the transceivers. (See abstract, Figure 1, col. 4, lines 23-68 to col. 5, lines 1-52).

As per claim 6, this method claim is rejected under similar rationale as set forth in the system claim 1.

As per claim 8, Waschura et al disclose a device, comprising:

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configurable logic configured with a pattern detector (80), the pattern detector configured for a test mode;

configurable input/output (24, 30) interconnect coupled to the configurable logic;

transceivers (20) coupled to the configurable input/output interconnect; and

the configurable input/output interconnect configured to communicate test signals received by at least one transceiver of the transceivers to the pattern detector. (See abstract, Figure 1, col. 4, lines 23-68 to col. 5, lines 1-52).

As per claims 9-10, Waschura et al further disclose: a test pattern accessible by the pattern detector, the pattern detector configured to compare the test signals to the test pattern to produce test data therefrom. (See col. 4, lines 51-64).

As per claims 14 and 22, Waschura et al disclose a channel test system, comprising:

a programmable logic device having programmable logic; and

configuration memory (24) coupled to the programmable logic device, the configuration memory for storing applications for channel testing (22), the applications for channel testing for configuring the programmable logic device to perform a respective test operations. (See Figs 1-2, col. 4, lines 43-64, col. 5, lines 53-68 to col. 6, lines 1-5).

As per claim 15, Waschura et al further disclose wherein at least an application of the applications for channel testing is for configuring the programmable logic with a pattern generator (40).

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As per claim 16, Waschura et al further disclose wherein at least an application of the applications for channel testing is for configuring the programmable logic with a pattern detector (80).

As per claim 23, this method claim is rejected under similar rationale as set forth in the system claim 14.

3. Claims 2-5, 7, 11-13, 17-21 and 24 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phung My Chung whose telephone number is 571-272-3818. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on 571- 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

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system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Phung My Chung  
Primary Patent Examiner